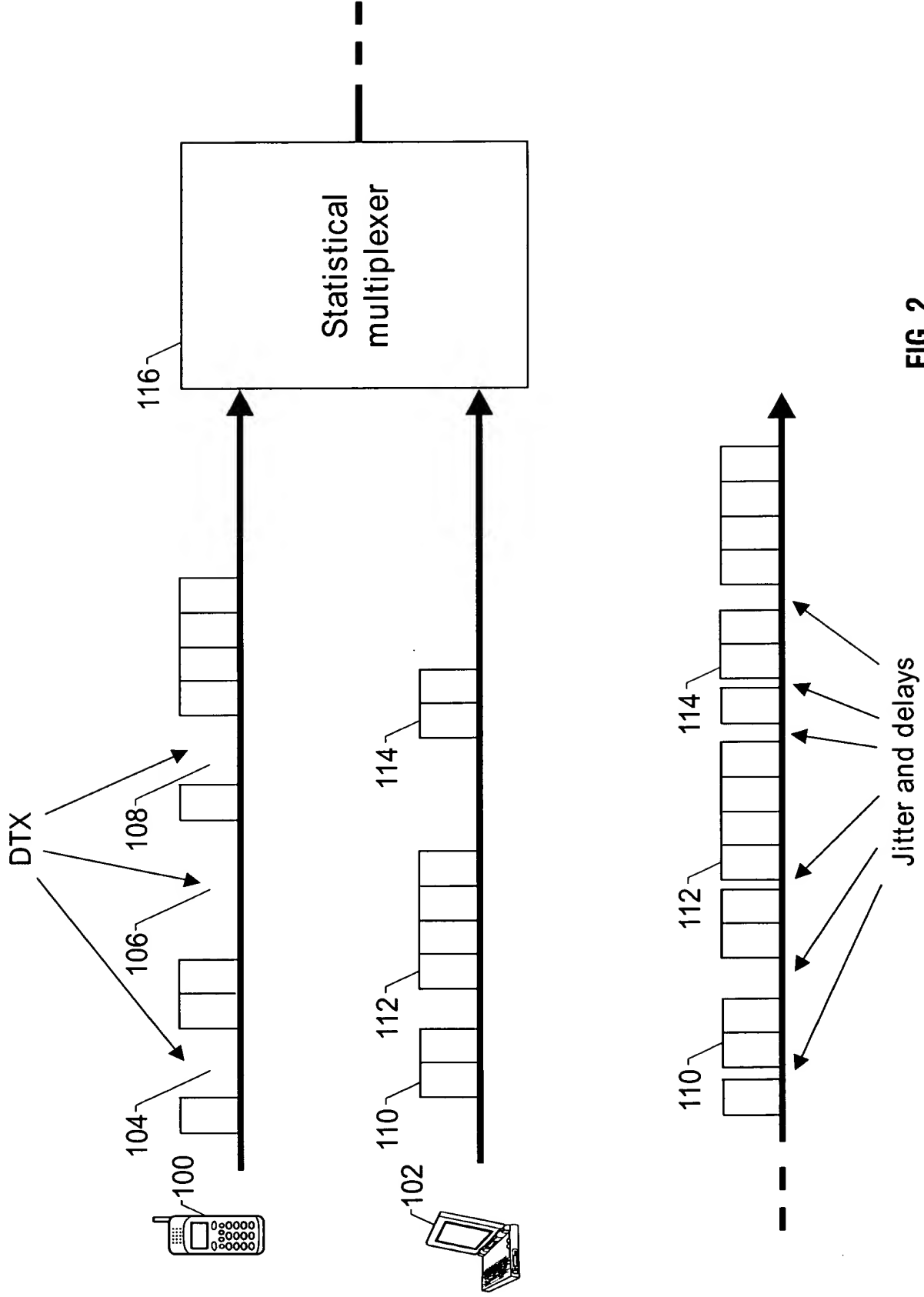


FIG. 1



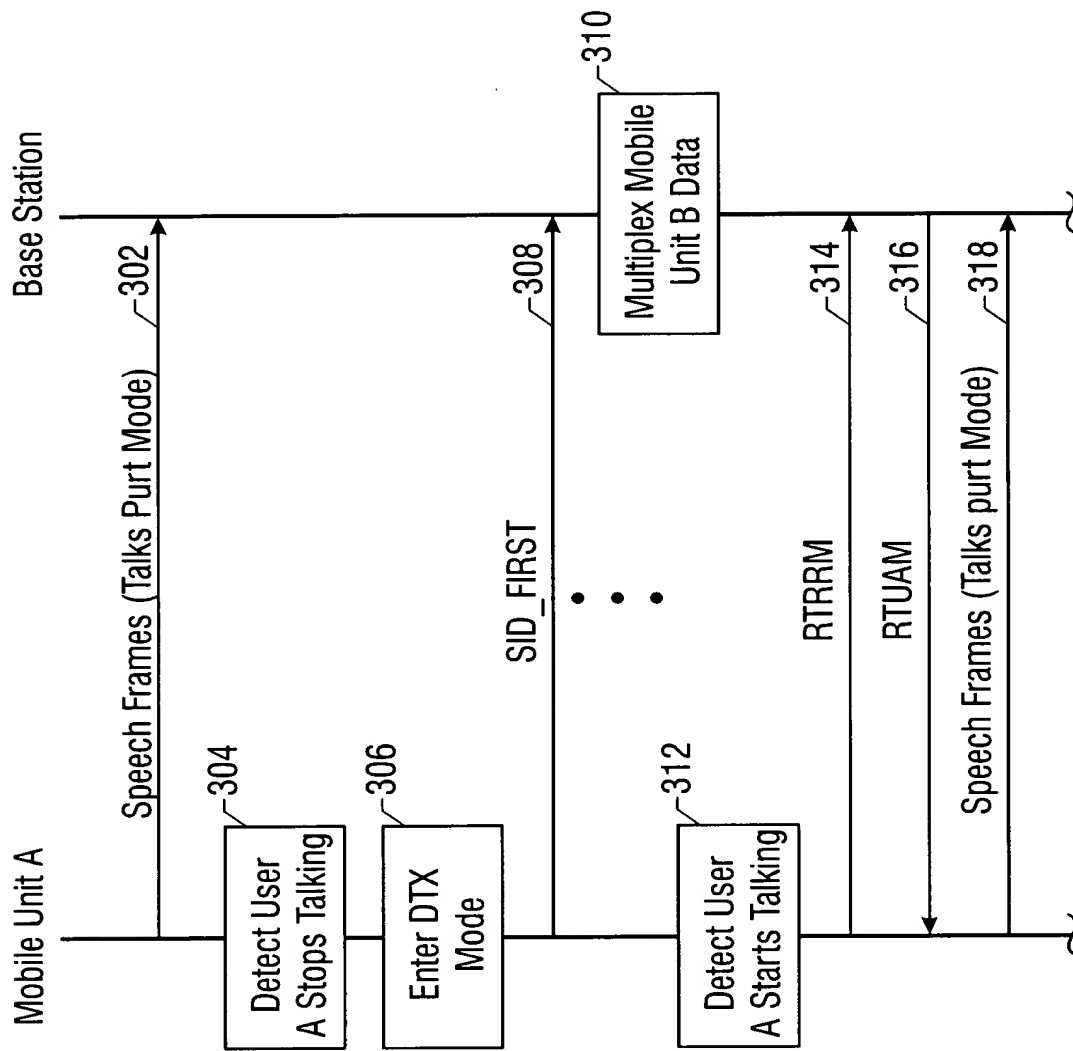


FIG. 3

UPLINK

DOWNLINK

FN	TN	UPLINK								TN	DOWNLINK							
		0	1	2	3	4	5	6	7		0	1	2	3	4	5	6	7
0		U1: SF #n-2 / SF #n-1								0	U3: DATA							
1		U1: SF #n-1 / SF #n								1	U3: DATA							
2		U1: SF #n-1 / SF #n								2	U3: DATA							
3		U1: SF #n / SF #n+1								3	U3: DATA							
4		U2: SF #k-2 / SF #k-1								4	U3: DATA							
5		U2: SF #k-1 / SF #k								5	U3: DATA							
6		U2: SF #k-1 / SF #k								6	U3: DATA							
7		U2: SF #k / SF #k+1								7	U3: DATA							
8		U1: SF #n / SF #n+1								8	U1: U							
9		U1: SF #n+1 / F								9	U1: U							
10		U1: SF #n+1 / F								10	U1: U							
11		U1: F / F								11	U1: U							
12		PTCCH								12	PTCCH							
13		U2: SF #k / SF #k+1								13	U3: DATA							
14		U2: SF #k+1 / SF #k+2								14	U3: DATA							
15		U2: SF #k+1 / SF #k+2								15	U3: DATA							
16		U2: SF #k+2 / SF #k+3								16	U3: DATA							
17		U3: DATA								17	U1: O / O							
18		U3: DATA								18	U1: O / SF #0							
19		U3: DATA								19	U1: O / SF #0							
20		U3: DATA								20	U1: SF #0 / SF #1							
21		U2: SF #k+2 / SF #k+3								21	U3: DATA							
22		U2: SF #k+3 / SF #k+4								22	U3: DATA							
23		U2: SF #k+3 / SF #k+4								23	U3: DATA							
24		U2: SF #k+4 / SF #k+5								24	U3: DATA							

FIG. 4A

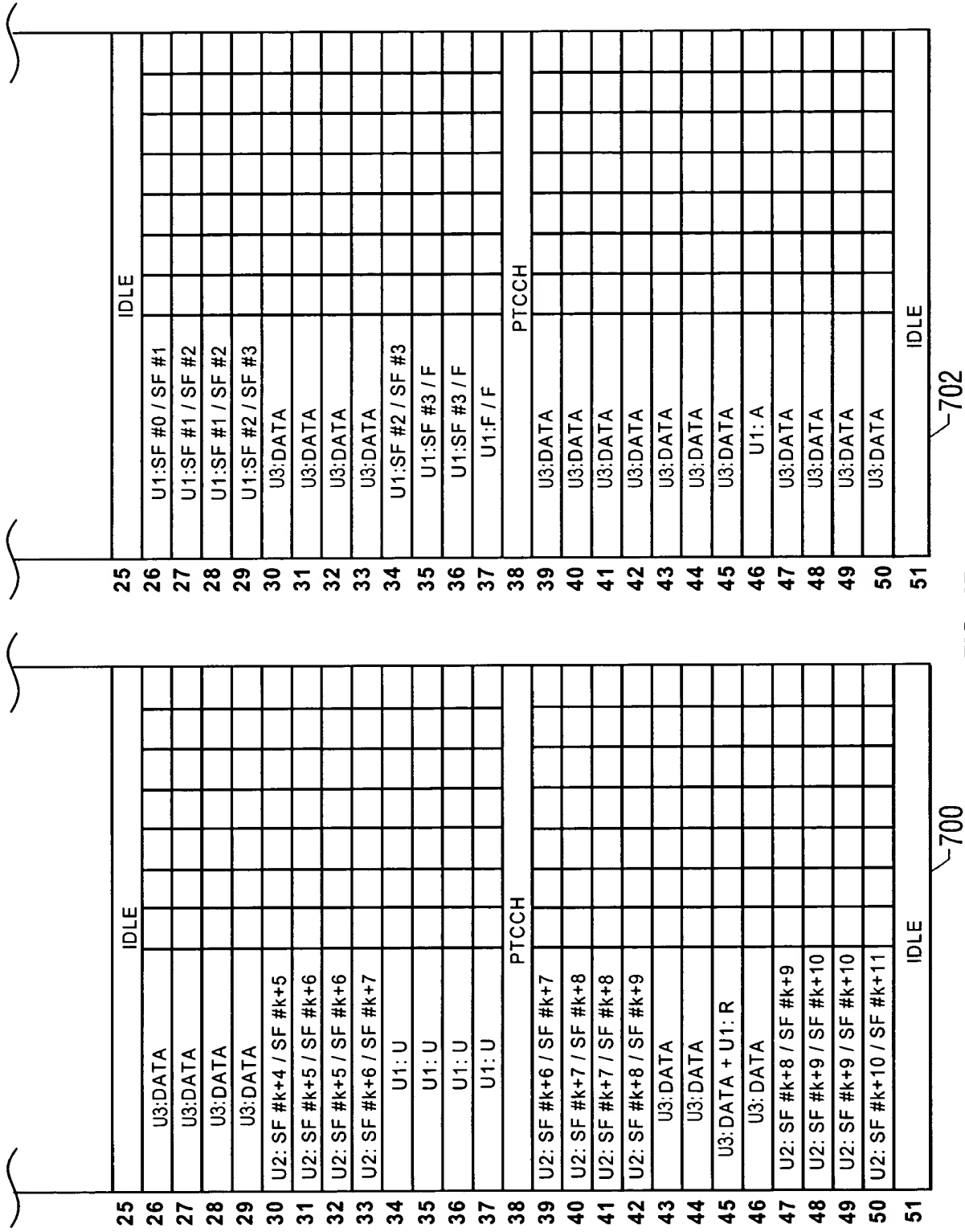


FIG. 4B

UPLINK

DOWNLINK

		UPLINK								DOWNLINK							
FN	TN	FN								TN							
		0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7
0		U1: SF #n-2 / SF #n-1								U3: DATA							
1		U1: SF #n-1 / SF #n								U3: DATA							
2		U1: SF #n-1 / SF #n								U3: DATA							
3		U1: SF #n / F								U3: DATA							
4		U2: SF #k-2 / SF #k-1								U3: DATA							
5		U2: SF #k-1 / SF #k								U3: DATA							
6		U2: SF #k-1 / SF #k								U3: DATA							
7		U2: SF #k / SF #k+1								U3: DATA							
8		U1: SF #n / F								U1: U							
9		U1: F / F								U1: U							
10		U1: F / F								U1: U							
11		U1: F / F								U1: U							
12		PTCCH								PTCCH							
13		U2: SF #k / SF #k+1								U3: DATA							
14		U2: SF #k+1 / SF #k+2								U3: DATA							
15		U2: SF #k+1 / SF #k+2								U3: DATA							
16		U2: SF #k+2 / SF #k+3								U3: DATA							
17		U3: DATA								U1: O / O							
18		U3: DATA								U1: O / SF #0							
19		U3: DATA								U1: O / SF #0							
20		U3: DATA								U1: SF #0 / SF #1							
21		U2: SF #k+2 / SF #k+3								U3: DATA							
22		U2: SF #k+3 / SF #k+4								U3: DATA							
23		U2: SF #k+3 / SF #k+4								U3: DATA							
24		U2: SF #k+4 / SF #k+5								U3: DATA							

FIG. 5A

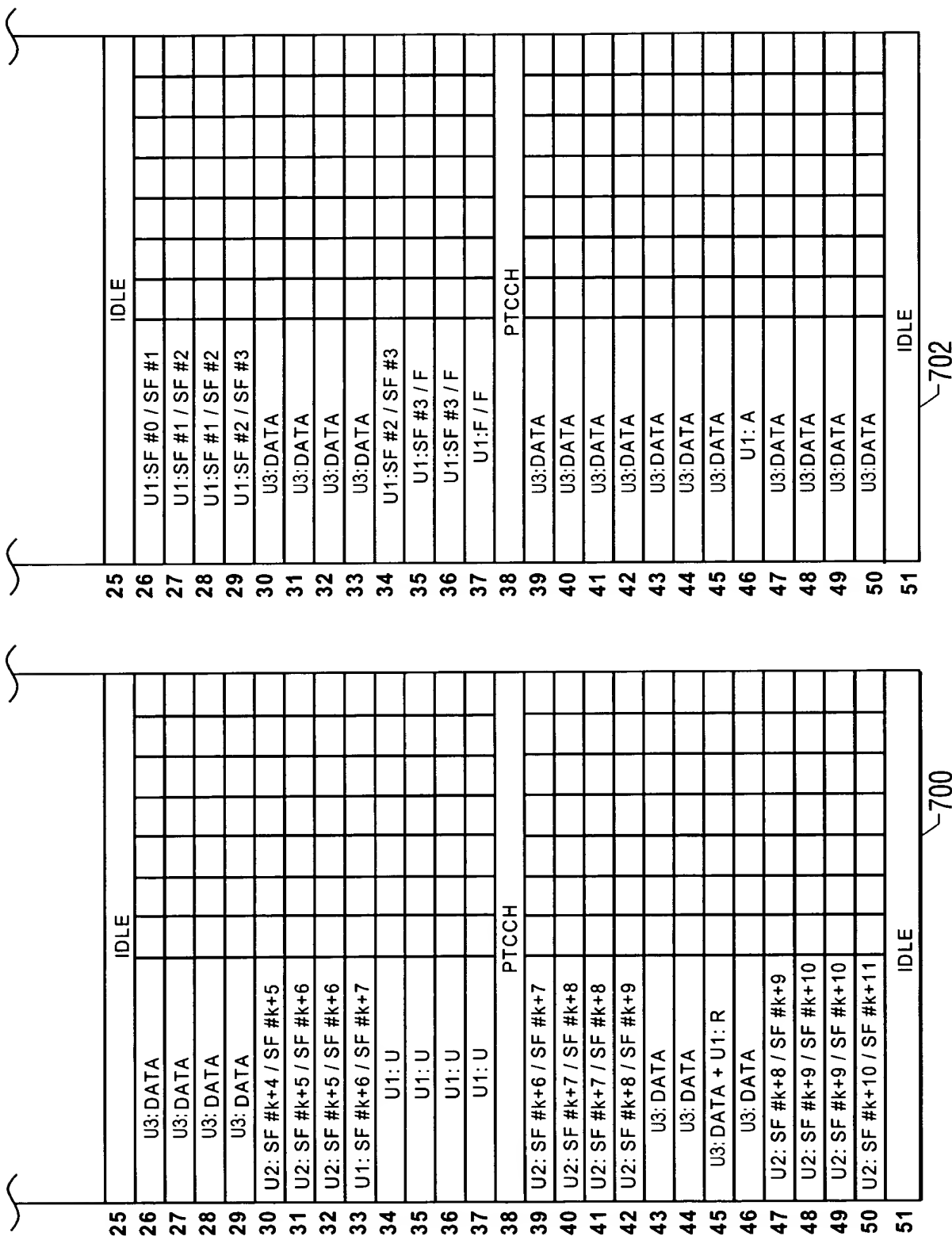


FIG. 5B

UPLINK									DOWNLINK											
	TN	0	1	2	3	4	5	6	7		TN	0	1	2	3	4	5	6	7	
FN	0	SF #0 / SF#1								FN	0	MB								
	1	SF #0 / SF#1									1	MB								
	2	SF #0 / SF#1									2	MB								
	3	SF #0 / SF#1									3	MB								
	4	SF #1 / SF#2									4	MB								
	5	SF #1 / SF#2									5	MB								
	6	SF #1 / SF#2									6	MB								
	7	SF #1 / SF#2									7	MB								
	8	SF #2 / F									8	U								
	9	SF #2 / F									9	U								
	10	SF #2 / F									10	U								
	11	SF #2 / F									11	U								
	12	PTCCH									12	PTCCH								
	13	MB									13	O / SF#0								
	14	MB									14	O / SF#0								
	15	MB									15	O / SF#0								
	16	MB									16	O / SF#0								
	17	MB									17	SF #0 / SF#1								
	18	MB									18	SF #0 / SF#1								
	19	MB									19	SF #0 / SF#1								
	20	MB									20	SF #0 / SF#1								
	21	U									21	SF #1 / SF#2								
	22	U									22	SF #1 / SF#2								
	23	U									23	SF #1 / SF#2								
	24	U									24	SF #1 / SF#2								

FIG. 6A

25	IDLE							
26	MB							
27	MB							
28	MB							
29	MB							
30	MB							
31	MB							
32	MB							
33	MB							
34	U							
35	U							
36	U							
37	U							
38	PTCCH							
39	MB							
40	MB							
41	MB+R							
42	MB							
43	O / SF#0							
44	O / SF#0							
45	O / SF#0							
46	O / SF#0							
47	SF #0 / SF#1							
48	SF #0 / SF#1							
49	SF #0 / SF#1							
50	SF #0 / SF#1							
51	IDLE							

Minimum Delay

25	IDLE							
26	SF #2 / SF#3							
27	SF #2 / SF#3							
28	SF #2 / SF#3							
29	SF #2 / SF#3							
30	SF #3 / SF#4							
31	SF #3 / SF#4							
32	SF #3 / SF#4							
33	SF #3 / SF#4							
34	SF #4 / F							
35	SF #4 / F							
36	SF #4 / F							
37	SF #4 / F							
38	PTCCH							
39								
40								
41								
42	A							
43								
44								
45								
46								
47	U							
48	U							
49	U							
50	U							
51	IDLE							

FIG. 6B

UPLINK									DOWNLINK								
TN	0	1	2	3	4	5	6	7	TN	0	1	2	3	4	5	6	7
FN									FN								
0	SF #0 / SF#1								0	MB							
1	SF #0 / SF#1								1	MB							
2	SF #0 / SF#1								2	MB							
3	SF #0 / SF#1								3	MB							
4	SF #1 / SF#2								4	MB							
5	SF #1 / SF#2								5	MB							
6	SF #1 / SF#2								6	MB							
7	SF #1 / SF#2								7	MB							
8	SF #2 / F								8	U							
9	SF #2 / F								9	U							
10	SF #2 / F								10	U							
11	SF #2 / F								11	U							
12	PTCCH								12	PTCCH							
13	MB								13	O / SF#0							
14	MB								14	O / SF#0							
15	MB								15	O / SF#0							
16	MB								16	O / SF#0							
17	MB								17	SF #0 / SF#1							
18	MB								18	SF #0 / SF#1							
19	MB								19	SF #0 / SF#1							
20	MB								20	SF #0 / SF#1							
21	U								21	SF #1 / SF#2							
22	U								22	SF #1 / SF#2							
23	U								23	SF #1 / SF#2							
24	U								24	SF #1 / SF#2							

FIG. 7A

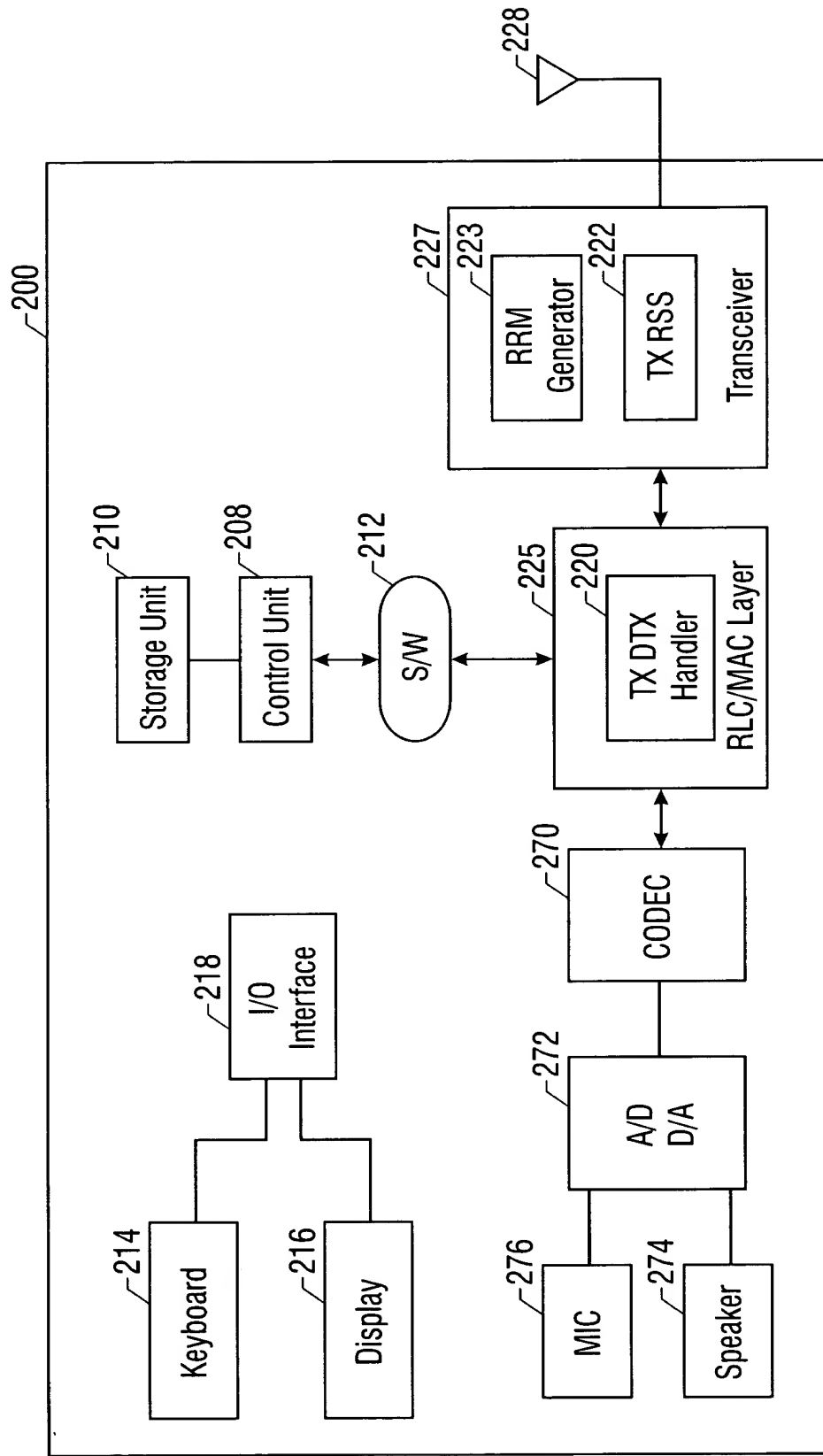


FIG. 8A

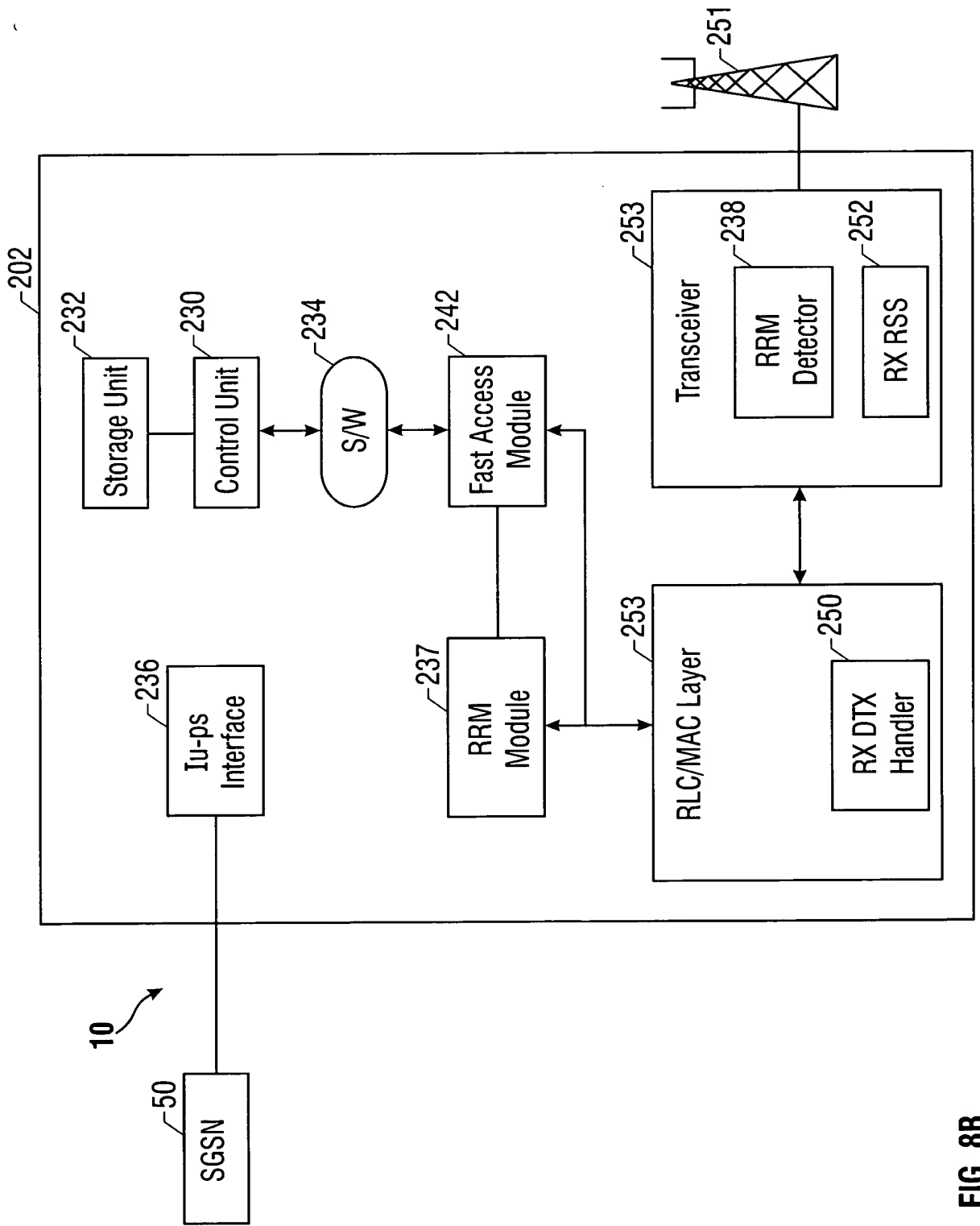


FIG. 8B

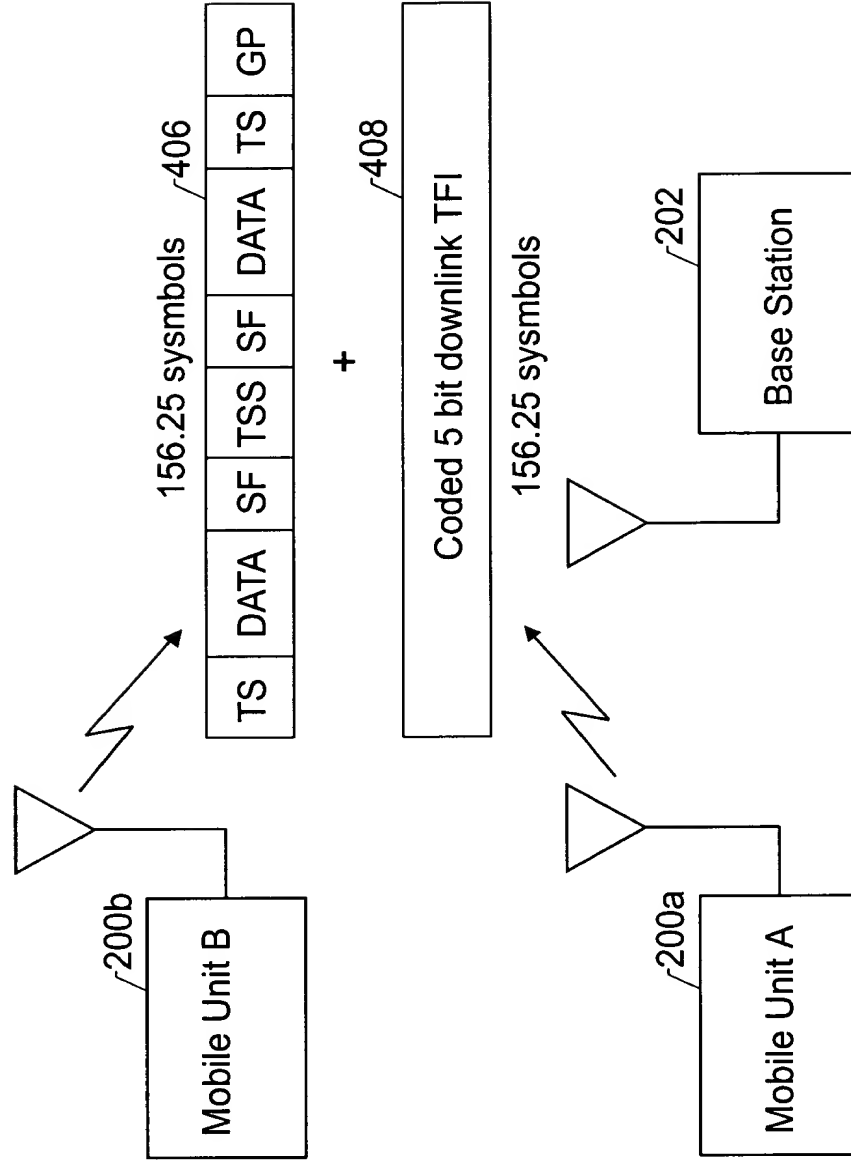


FIG. 9

RTUAM

500

502

< RTFACCH Uplink Assignment Message

Message content > :: =

< **TFI**: bit (5) >

{0|1 < **Uplink_TFI_ASSIGNMENT**: bit (5) >}

< **TSC**: bit (3) >

< **ARFCN**: bit (10) >

< **TIMESLOT_ALLOCATION**: bit (8) >

< padding bits >;

FIG. 10

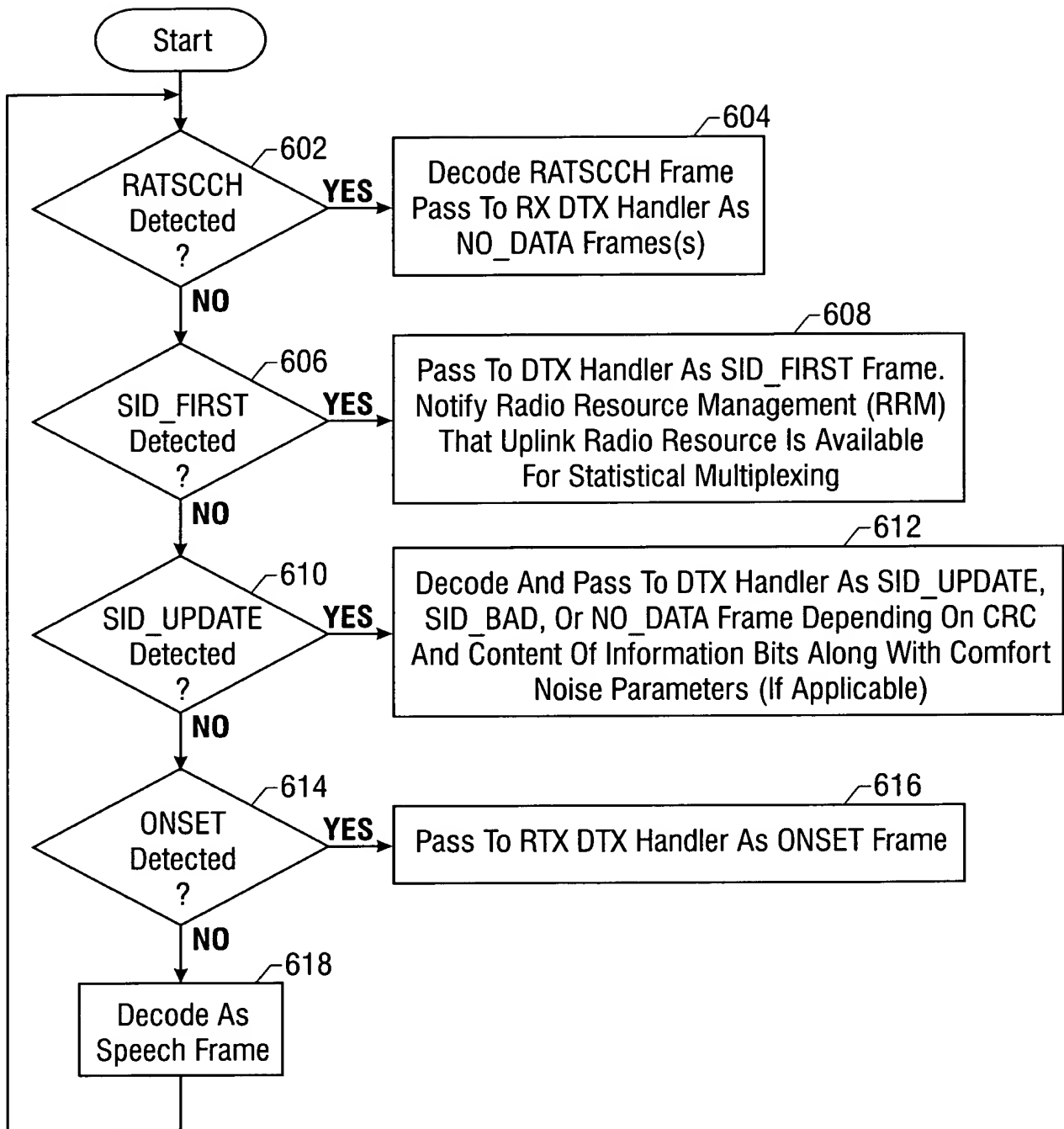


FIG. 11

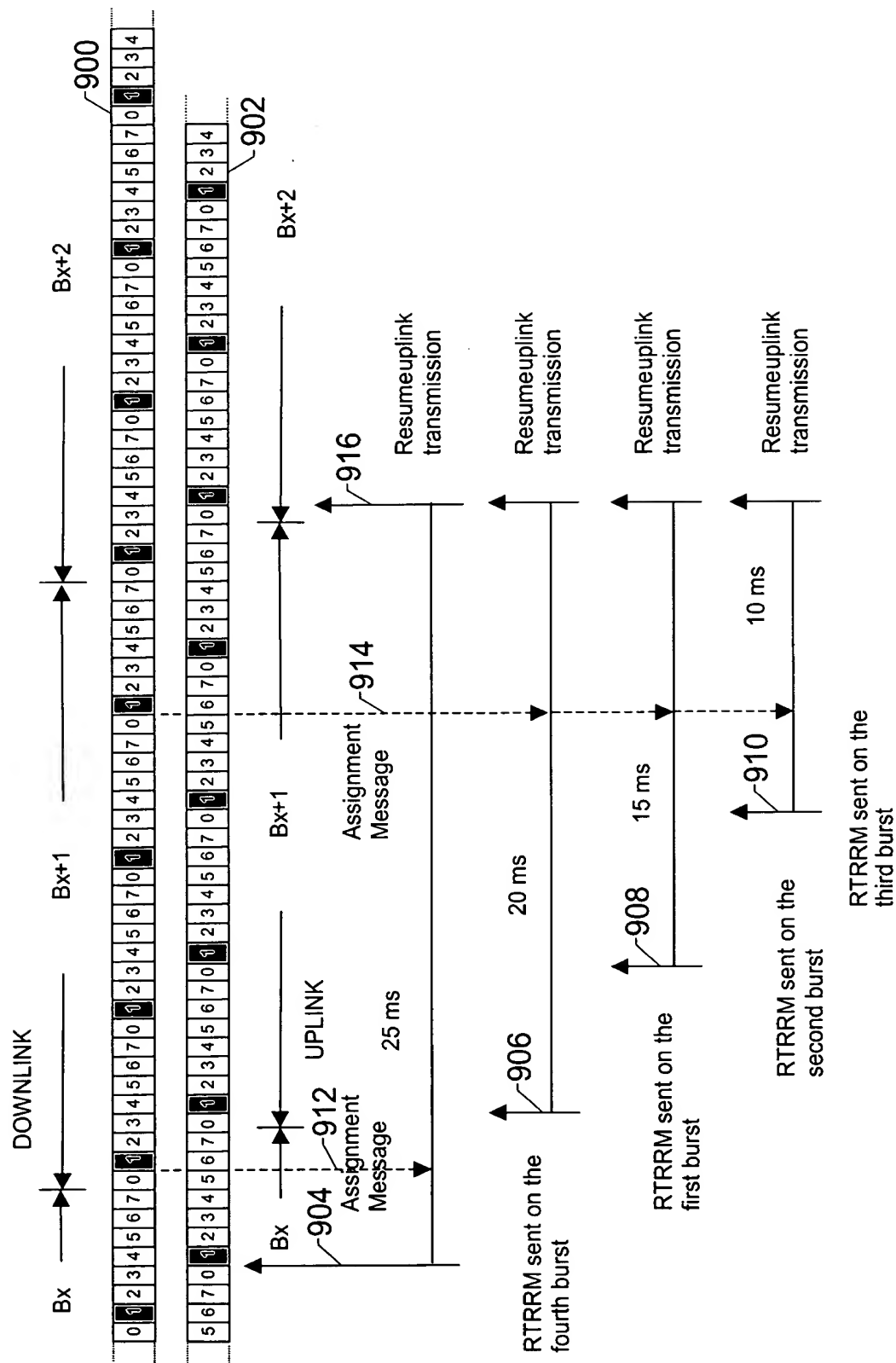


FIG. 12

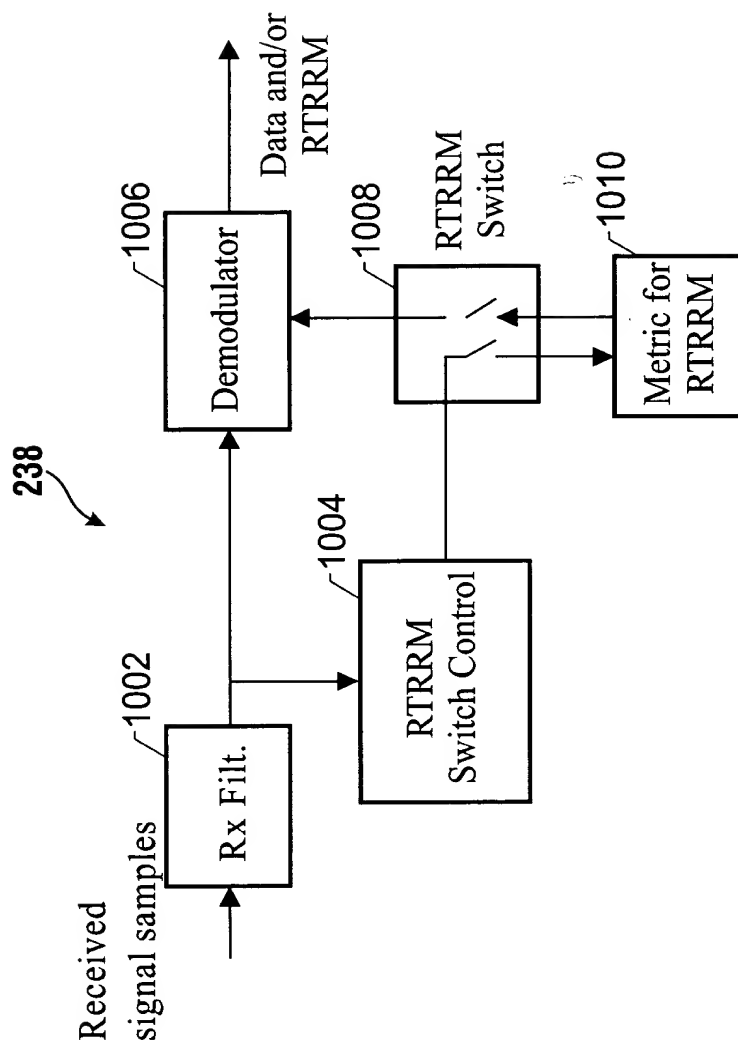


FIG. 13